



arm

We believe in making
technology more accessible
to more people.

70%

of the world's population
uses Arm technology

The Architects of Global Possibilities

The global leader in the development
of licensable technology

- R&D outsourcing for semiconductor companies

Focused on freedom and
flexibility to innovate

- Technology reused across multiple applications

With a partnership based
culture & business model

- Licensees take advantage of learnings from a
uniquely collaborative ecosystem

1,650+

licenses, growing by
>100 every year

138+bn

Arm-based chips shipped
to-date

6.2bn

Arm-based chips shipped
in Q3 FY2018

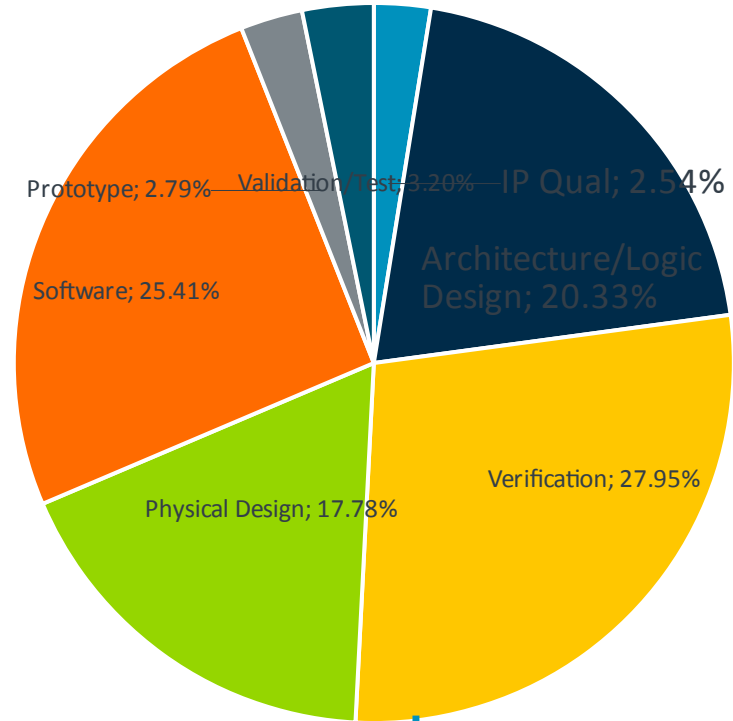
525+
Licensees

Industry leaders and high-growth
start-ups; chip companies and OEMs

IP enablement for all

- Where the ecosystem helps

SoC development cycle cost (\$%)



Verification

- Verification IP
- Models
- Emulation/FPGA flows
- Arm approved design Services
- Support & training

Software

- Debug tools
- Compilers
- Firmware
- Apps
- OS
- Open source contributions
- Models
- FPGA/Emulation support
- Software development boards

Physical design

- Reference methodologies
- Physical IP and POP
- DFT/DFM readiness and flows
- Arm approved design services

Silicon IC prototyping

- Test methodologies
- Models
- Debug tools
- Evaluation boards
- Ready to use firmware or OS

IP qualification

- Silicon proven IP
- Functional integration kits
- Reference platforms
- Models for simulation
- Models for software acceleration
- Support

Architecture/design

- Integration kits
- Reference platforms
- Models
- Bus & interface standards
- Arm system IP
- Functional safety support
- Security – IP, software and platform
- Emulation

Arm DesignStart

- fast cost effective access to Arm IP and ecosystem

DesignStart for custom SoCs/ASICs

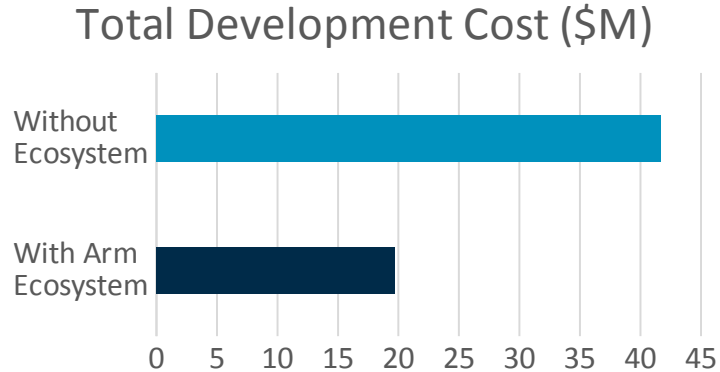
- ✓ Cortex-M0 and Cortex-M3 CPUs for no upfront fee
- ✓ Cortex-A5 CPU: lowest-cost route to Linux-capable design
- ✓ 1,000s of physical IP libraries

DesignStart FPGA

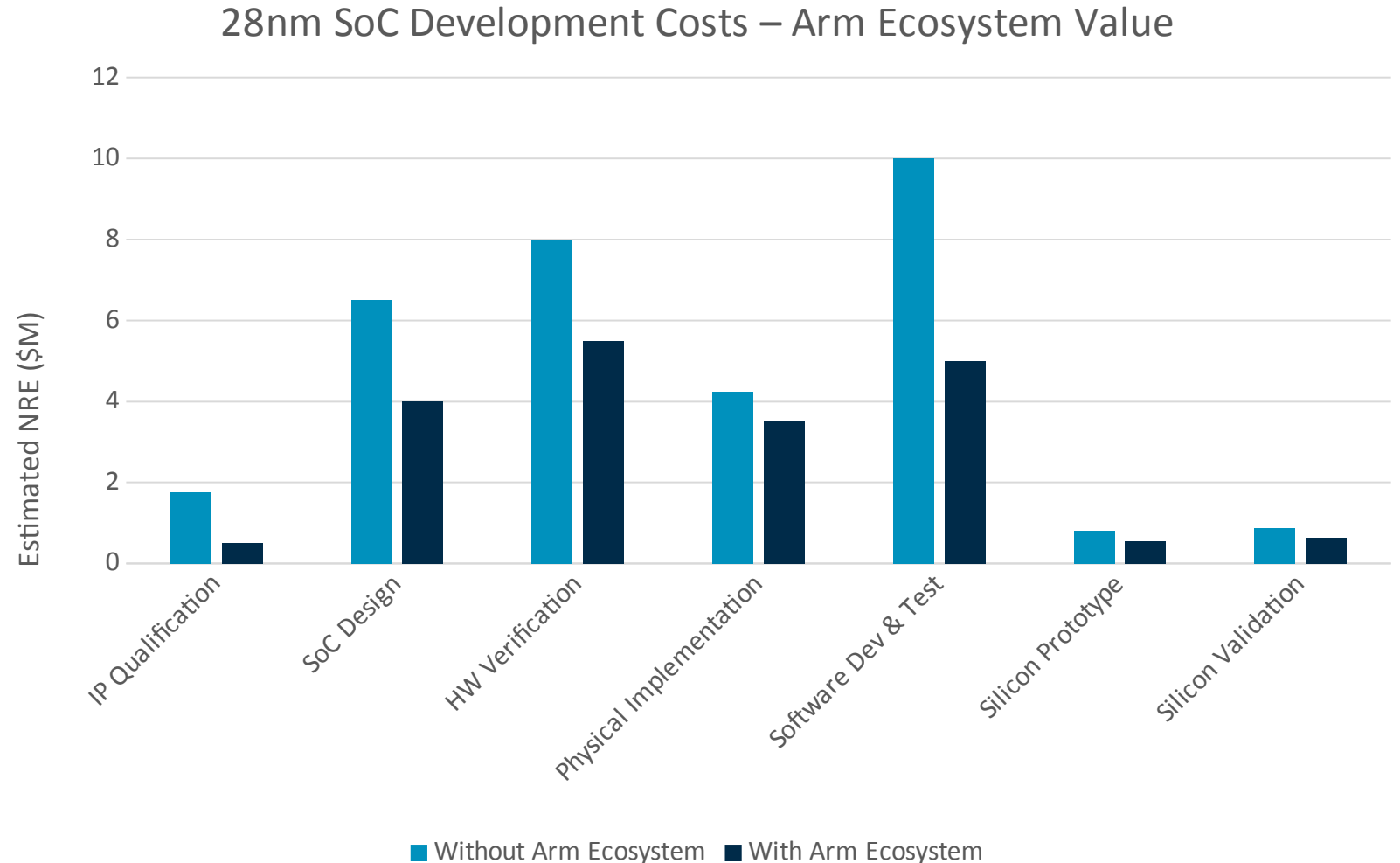
- ✓ Cortex-M1 and Cortex-M3 CPUs
- ✓ Soft IP in Xilinx FPGA
- ✓ No license fee, no royalties

The Arm ecosystem

- Marking IP use more affordable to the industry



- Data is based on a **medium complexity** 28nm SoC grounds up SoC design
- Value of Arm ecosystem is an **estimate** of **engineering hours saved** due to ecosystem contribution
- Costs are **estimates** based on **analyst data** and **Arm estimations**
- Does not include potential upside:
 - lost opportunity cost
 - EDA license/infrastructure cost savings



Arm is committed to Open Source innovation

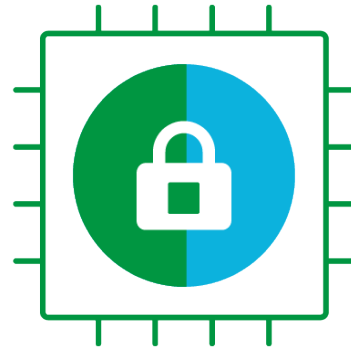
Contributing to around 200 Open Source projects



+ **500+**

+ **110** Arm-based boards

Building secure foundations



Trustedfirmware.org

+ **Secure World**
Software

Collaborating across the ecosystem

+ _____

+ **top three contributors**



arm

Thank You

Danke

Merci

谢谢

ありがとう

Gracias

Kiitos

감사합니다

धन्यवाद

תודה

We believe that
the whole is
greater than the
sum of its parts.